



EMW3072模式控制引脚定义

	MODE_SEL3 DIG_PAD_10	MODE_SEL2 DIG_PAD_15	MODE_SEL1 DIG_PAD_14	MODE_SEL0 PAD_SEL
Boot with Flash	0	0	0	0
Boot with UART	0	0	1	0
GPIO download Flash	1	0	0	1

SPI烧录用到的引脚定义

(wp, hold引脚在BASE板有10K电阻上拉置高)

	Function Description
DIG_PAD[0]	gpio_flc_csn
DIG_PAD[1]	gpio_flc_si
DIG_PAD[2]	gpio_flc_so
DIG_PAD[3]	gpio_flc_sclk
DIG_PAD[4]	gpio_flc_wp
DIG_PAD[5]	gpio_flc_hold
DIG_PAD[6]	NA
DIG_PAD[7]	NA
DIG_PAD[8]	NA
DIG_PAD[9]	NA
DIG_PAD[10]	MODE_SEL3
DIG_PAD[11]	NA
DIG_PAD[12]	NA
DIG_PAD[13]	NA
DIG_PAD[14]	MODE_SEL1
DIG_PAD[15]	MODE_SEL2

EMW3072_GPIO_PIN_MUX

Num	Pin Name	GPIO Func=0	GPIO Func=1	GPIO Func=2	GPIO Func=3	GPIO Func=4
1	DIG PAD0	GPIO0	UART0_TXD	SWC	SPI1_CSN	PWM5
2	DIG PAD1	GPIO1	UART0_RXD	SWD	SPI1_SCK	PWM7
3	DIG PAD2	GPIO2	UART1_TXD	UART1_TXD	SPI1_MISO	I2C0_SCL
4	DIG PAD3	GPIO3	UART1_RXD	UART1_RXD	SPI1_MOSI	I2C0_SDA
5	DIG PAD4	SWC	GPIO4	SDIO_CMD	UART0_TXD	PWM0
6	DIG PAD5	SWD	GPIO5	SDIO_CLK	UART0_RXD	PWM2
7	DIG PAD6	GPIO6	SPI0_CSN	SDIO_DATA0	UART0_CTS	PWM4
8	DIG PAD7	GPIO7	SPI0_SCK	SDIO_DATA1	UART0_RTS	PWM8
9	DIG PAD8	GPIO8	SPI0_MOSI	SDIO_DATA2	I2C1_SCL	UART1_TXD
10	DIG PAD9	GPIO9	SPI0_MISO	SDIO_DATA3	I2C1_SDA	UART1_RXD
11	DIG PAD10	MODE_SEL3	PWM1	GPIO10	UART2_CTS	SPI2_SCK
12	DIG PAD11	GPIO11	PWM3	SDIO_INT	UART2_RTS	SPI2_MOSI
13	DIG PAD12	GPIO12	GPIO12	SPI2_CSN	UART2_TXD	GPIO12
14	DIG PAD13	GPIO13	GPIO13	SPI2_MISO	UART2_RXD	GPIO13
15	DIG PAD14	STRAP/SEL1	PWM0	SPI2_SCK	UART1_CTS	GPIO14
16	DIG PAD15	STRAP/SEL2	PWM2	SPI2_MOSI	UART1_RTS	GPIO15

Title: MXKIT Core3072

Designer: _____ Verifier: _____

Approver: _____ Size: A3 Ver: V5.0

Data: * Sheets: 1 of 1 Secrecy: Y

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